

Implementation of ZVS interleaved booster converter in Electric vehicles with reduced THD

Abubaker Emheisen^{1*}, Mustafa Emheisen²

^{1,2} Electrical and Electronic Engineering Department, Bani Waleed University, Libya

تنفيذ محول معزز متداخل ZVS في المركبات الكهربائية مع تقليل THD

أبو بكر إمحيسن^{1*}، مصطفى إمحيسن²

^{2,1} قسم الهندسة الكهربائية والإلكترونية، جامعة بني وليد، ليبيا

*Corresponding author: aemheisen@gmail.com

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Abstract

With the increase in global infrastructure, electric vehicles (EVs) are the replacement for many conventional transportation. In recent years, there has been significant development in EV technology to increase efficiency and reduce the THD of the machine. This paper proposes a new converter topology ZVS interleaved booster converter, for traction purposes when connected to the smart grid. The analysis and performance with complete modes of operation is been discussed with a graphical representation of outputs of the DC machine. The modeling and analysis are done using MATLAB Simulink.

Keywords: ZVS, Electric vehicles, THD.

المخلص

مع ازدياد البنية التحتية العالمية، أصبحت المركبات الكهربائية بديلاً للعديد من وسائل النقل التقليدية. في السنوات الأخيرة، شهدت تكنولوجيا المركبات الكهربائية تطوراً ملحوظاً لزيادة كفاءتها وتقليل التشوه التوافقي الكلي (THD). تقترح هذه الورقة البحثية طوبولوجياً جديدة لمحول التعزيز المتداخل ZVS، لأغراض الجر عند توصيله بالشبكة الكهربائية الذكية. تمت مناقشة التحليل والأداء مع جميع أوضاع التشغيل من خلال تمثيل بياني لمخرجات آلة التيار المستمر. تم إجراء النمذجة والتحليل باستخدام MATLAB Simulink.

الكلمات المفتاحية: ZVS، المركبات الكهربائية، THD.

Introduction

A general hybrid electrical vehicle utilizes huge battery banks with large capacities. The discharging characteristics of the battery are also very much higher efficient than the conventional technologies. The application of such traction drives in an electrical bus system is a challenge where the power quality of the grid system has to be maintained. We consider a smart grid where EV is used as a traction element or for transportation. Application of smart grids ensures the reliability of power and maintains the affordability of the devices for the operation. A simple smart grid with EV application is shown in Figure 1.

Figure 1 defines the utilization of power electronics in an EV with boost capabilities with a ZVS interleaved booster converter with reduced THD (Total Harmonic Distortion). The machine used to drive the load is a three-phase induction machine. This plug-in electric vehicle uses a high-voltage battery to charge it through an external supply from the smart grid. There are two stages in this converting operation: the rectifier stage, where the battery gets charged up, and an inversion stage, where the load, i.e. An induction motor is operated from the battery supply of power. A complete conversion block diagram is given in Figure 2.

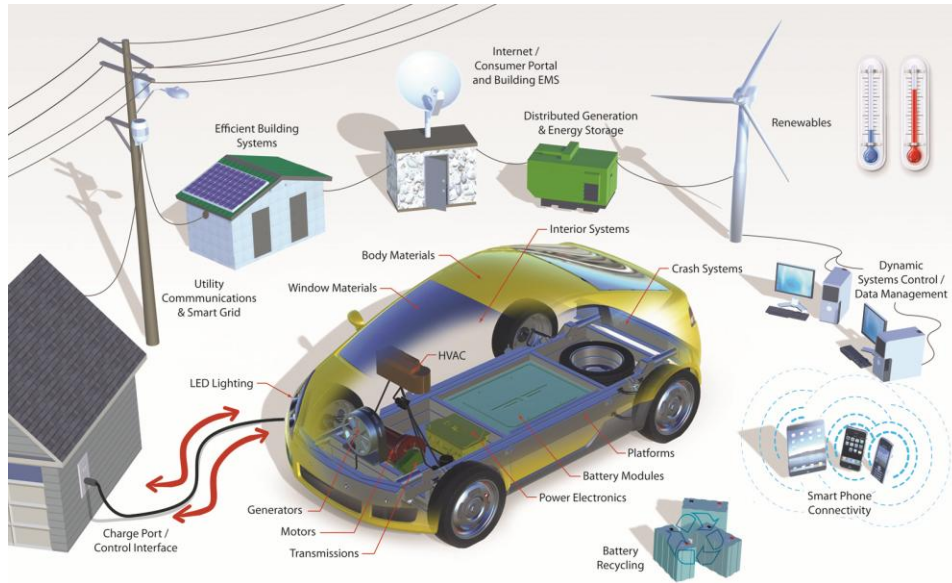


Figure 1: Electric vehicle in Smart grid application.

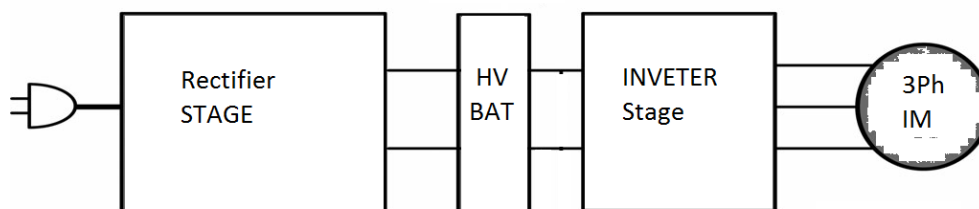


Figure 2: Block diagram of the converter.

In the interleaved booster converter, there is a continuous current conduction mode (CCM) basically used to realize Power factor correction (PFC). The rectifier stage includes a Diode Bridge Rectifier (DBR) to convert AC into rippled DC fed to a double booster converter with two boosting inductors. For the boosting operation MOSFETs are used due to high switching frequency operation. The output of the converter has a very high gain around 4 times of the input DC voltage from the DBR. The schematic of the interleaved PFC booster converter is shown in Figure3 below.

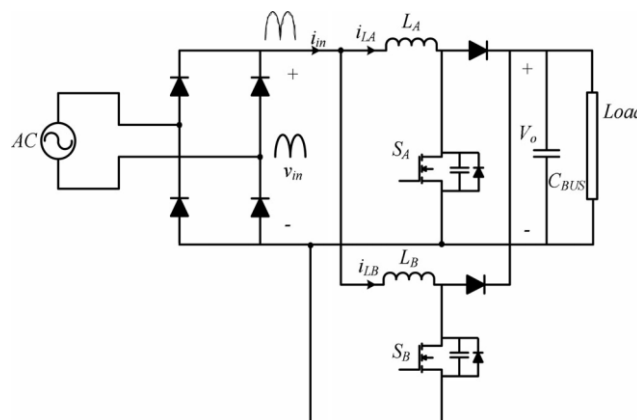


Figure 3: Interleaved PFC booster converter.

With the switching of the power electronic switches MOSFETs with a controlled duty ratio the output boost voltage is controlled. Zero voltage switching (ZVS) is the secondary circuit that has to be implemented which further includes in the interleaved booster converter. An auxiliary circuit with PWM (Pulse width Modulation) ZVS operation is basically of two types, one of them is resonant and the other is non-resonant. The ZVS with auxiliary circuit with interleaved PFC converter is shown in Figure 4.

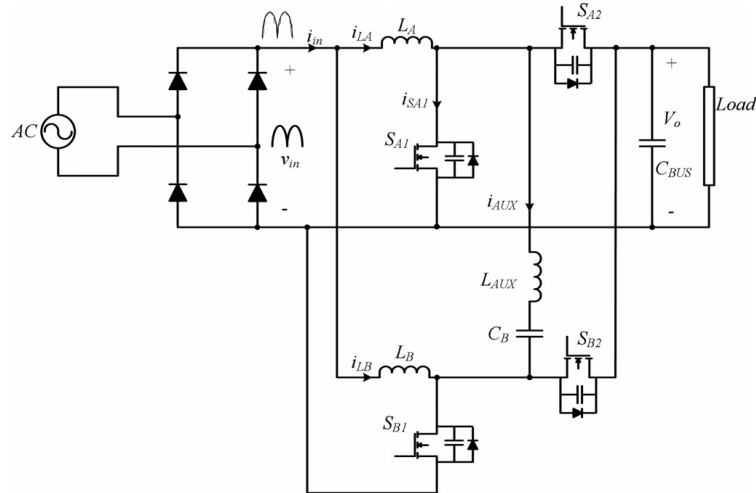


Figure 4: Auxiliary ZVS circuit with interleaved booster converter.

The auxiliary circuit is a combination of two MOSFET switches and a series auxiliary passive element combination with an inductor and a capacitor.

Analysis of ZVS Interleaved Booster Converter

The ZVS operation is done with change in the switching states for every 180° phase shift to reduce the ripple in the current of the converter. The auxiliary circuit will be in resonance as there is a high frequency inductor with a blocking capacitor in order to eliminate DC currents increment in the main switches of the converter. The switching states of the all the four MOSFETs is as below in Figure 5.

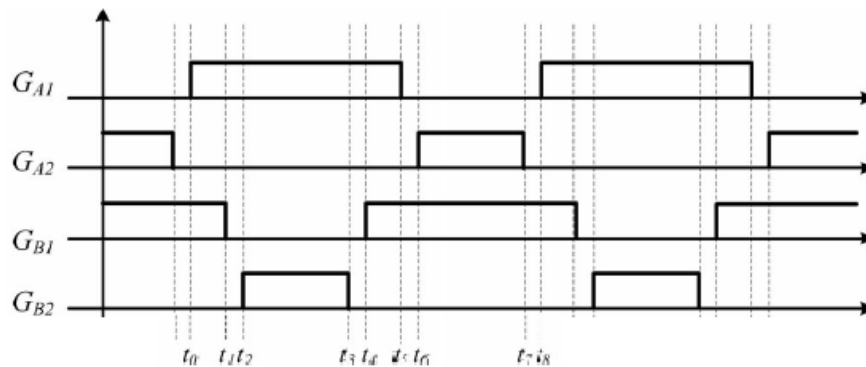


Figure 5: Switching states of MOSFETs.

The operating modes are explained below at different instants of time.

Mode I:

This mode is between the time instants as $t_0 < t < t_1$ where the switches SA1 and SB1 will be in ON state. The current through the switch is given as

$$i_{SA1}(t) = I_V - I_{Aux,p} - \frac{v_{in}}{L_A}(t - t_0)$$

Mode II:

This mode is between the time intervals $t_1 < t < t_2$ where the auxiliary circuit is in operation and the current through the aux circuit is given as

$$i_{AUX}(t) = I_{Aux,p} - \frac{V_o}{2(t_2 - t_1)L_{AUX}}(t - t_1)^2$$

Mode III:

This mode is between $t_2 < t < t_3$ the switch SB2 is turned ON under ZVS when both the capacitors of SB1 and SB2 are fully charged. The current through the auxiliary circuit at this mode is given as

$$i_{AUX} = I_{Aux,p} - \frac{V_o}{2L_{AUX}}t_d - \frac{V_o}{L_{AUX}}(t - t_2)$$

Mode IV:

During this time state ($t_3 < t < t_4$) the output capacitor SB2 is discharging and the output capacitor SB1 is charging to a value of V_o . The auxiliary current is

$$i_{AUX}(t) = I_{AUX,p} + \frac{3V_o}{2L_{AUX}}t_d$$

Mode V:

The switches SA1 and SB1 are in ON state during this mode $t_4 < t < t_5$ where the voltage across the aux inductor is zero and the current remains constant.

$$i_{LA}(t) = I_V + \frac{v_{in}}{L_A}(t - t_0)$$

Mode VI:

The output capacitor of SA2 is discharging from V_o and output capacitor of SA1 is charging to V_o during $t_5 < t < t_6$.

$$i_{AUX}(t) = -I_{AUX,p} + \frac{V_o}{2t_d L_{AUX}}(t - t_5)^2$$

Mode VII:

During this time interval $t_6 < t < t_7$ the voltage across the aux inductor is V_o so the current through the aux circuit is given as

$$i_{AUX}(t) = -I_{AUX,p} + \frac{V_o}{2L_{AUX}}t_d + \frac{V_o}{L_{AUX}}(t - t_6)$$

Mode VIII:

In this operating mode ie., between $t_7 < t < t_8$ the output capacitor of SA2 is charging to V_o and the output capacitor of SA1 is discharging from V_o . The aux current at this instant of time is given as

$$i_{AUX}(t) = -I_{AUX,p} + \frac{V_o}{2L_{AUX}}t_d$$

Simulink Design and Outputs

The complete analysis is carried out in MATLAB Simulink. The design of the smart grid and ZVS interleaved booster converter is shown below in Figures 5 and 6.

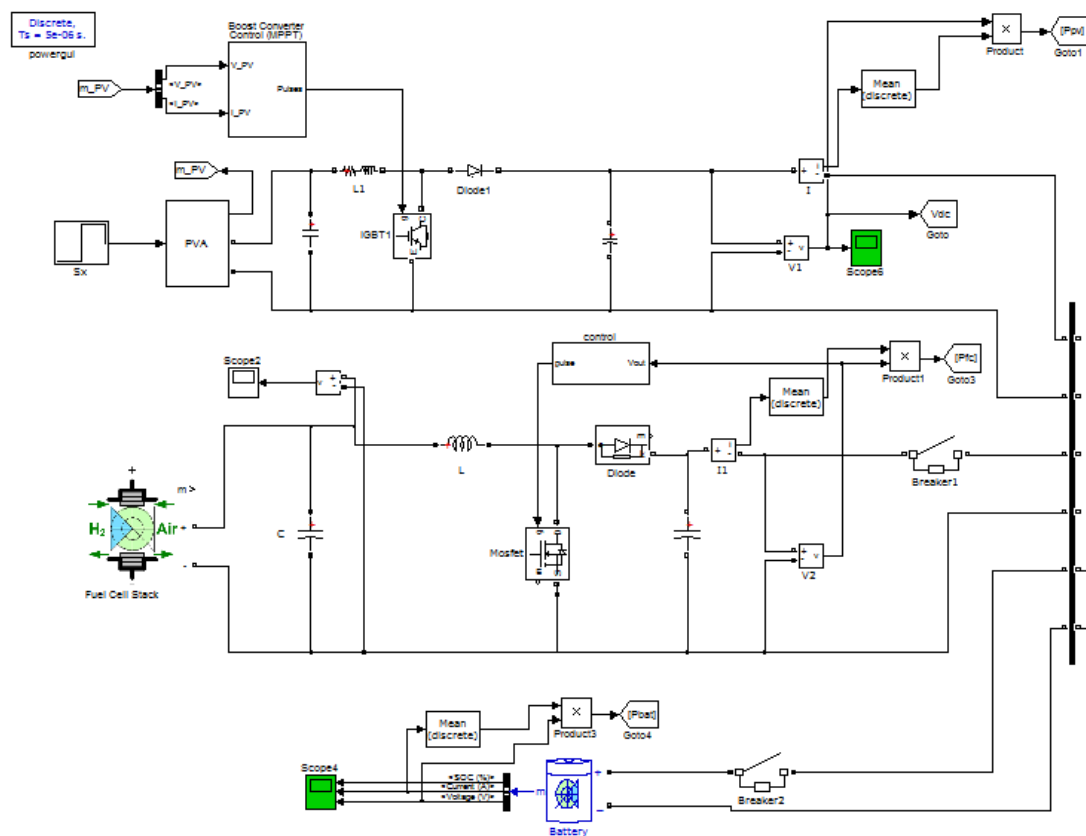


Figure 6: Smart grid Simulink design.

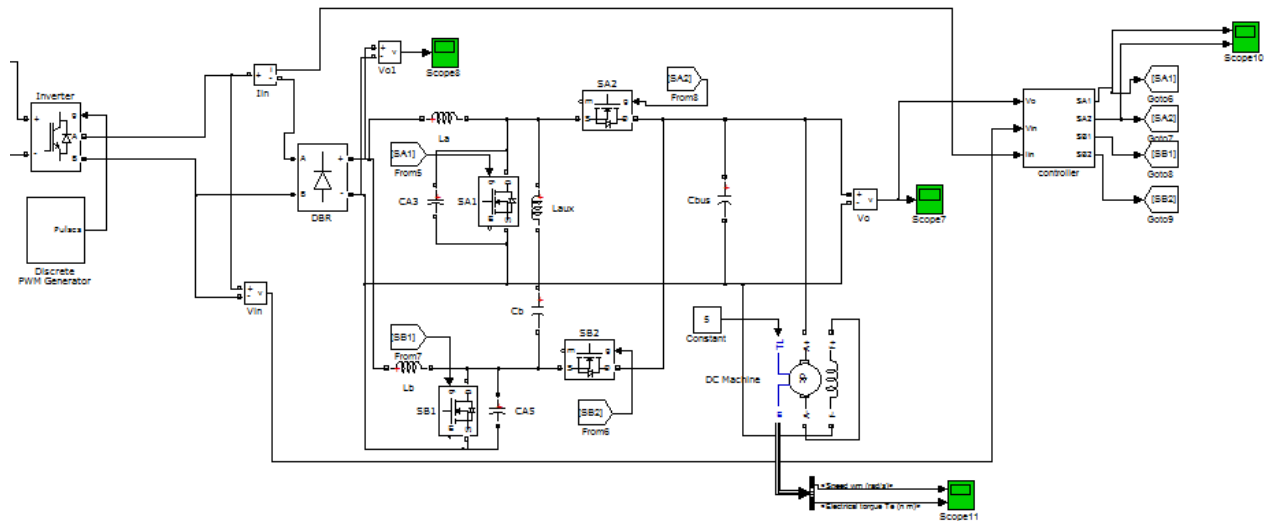


Figure 7: ZVS interleaved booster converter with EV application.

The smart grid is a combination of three different renewable sources, which include PVA, a Fuel cell and a battery. All three sources are connected through DC-DC converters to a DC bus. The voltage of the DC bus is shown in Figure 8, defining the voltage at approximately 400V. The DC bus. After the DC bus an inverter is utilized to convert DC to AC voltage. The AC voltage is now fed to the ZVS interleaved booster converter.

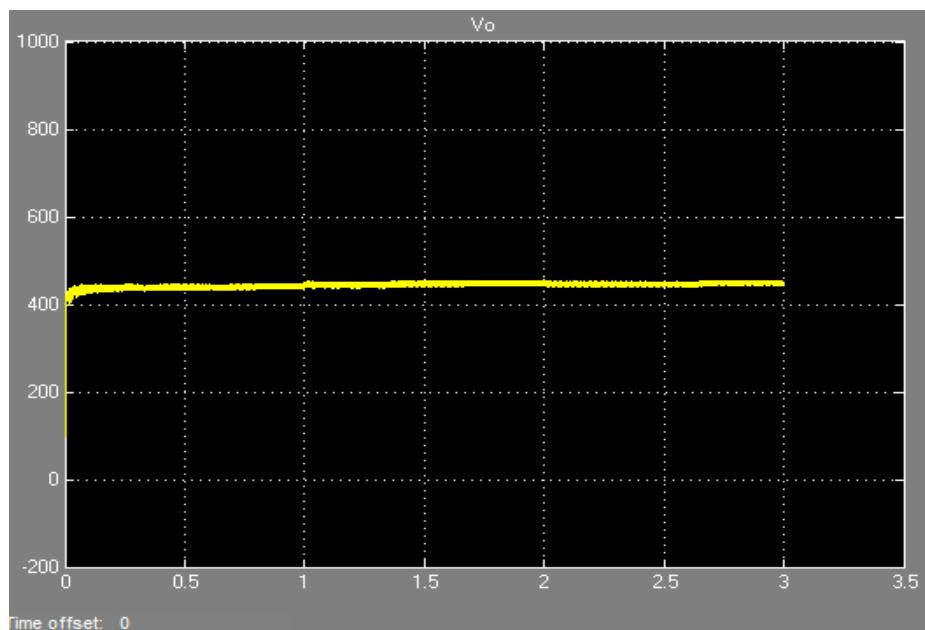


Figure 8: Voltage of DC bus.

Power outputs of each source are shown in Figure 9 where the Ppv is blue, Pfc is green and the red is the battery power Pbat in charging state. The power of the battery is in negative as the state of charge if the battery is 40% tending to charge from the renewable sources available.

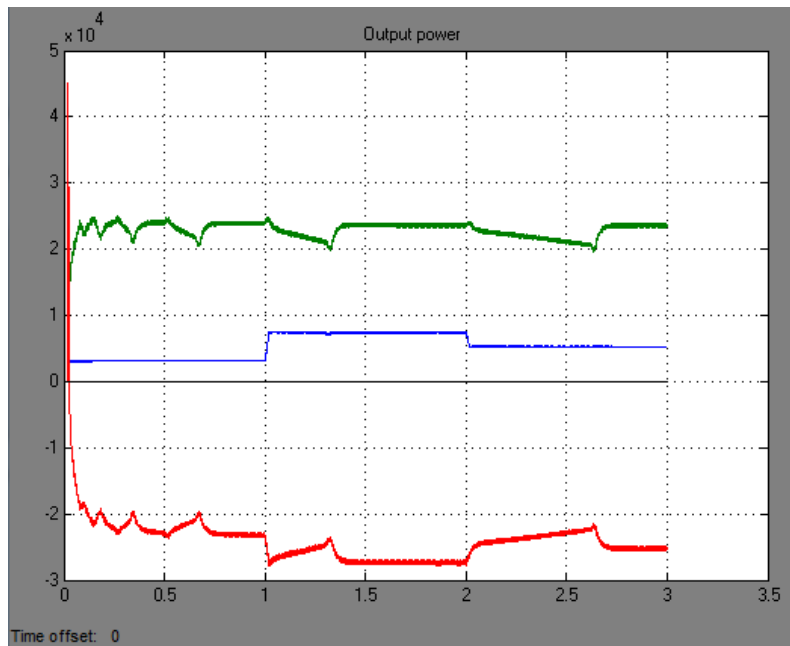


Figure 9: Output power of sources.

With further connections to the system, a full bridge IGBT inverter is attached to the DC bus employing PWM technique to convert DC to AC voltage. The ZVS interleaved booster converter with DC machine is attached to the inverter for a boosted DC voltage fed to the DC motor. The voltage output of the interleaved booster converter can be seen in Figure 10 with less ripple during load conditions.

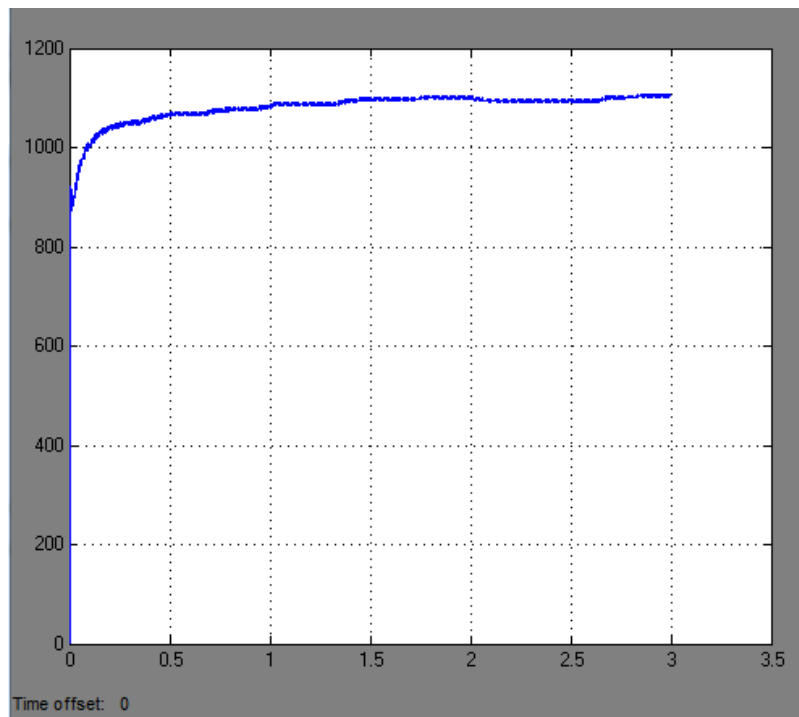


Figure 10: DC output voltage of ZVS interleaved booster converter.

The output boosted voltage of 1100V from the output of the converter is now fed to the DC motor and the operating characteristics are displayed with respect to time in Figure 11. The graph shows the speed (ω rad/sec) of the DC machine compared to the electromagnetic torque (T_e Nm) where the torque is very much high during the start to overcome the inertia. The speed initializes from 0 rad/sec and gradually settles at 650 rad/sec.

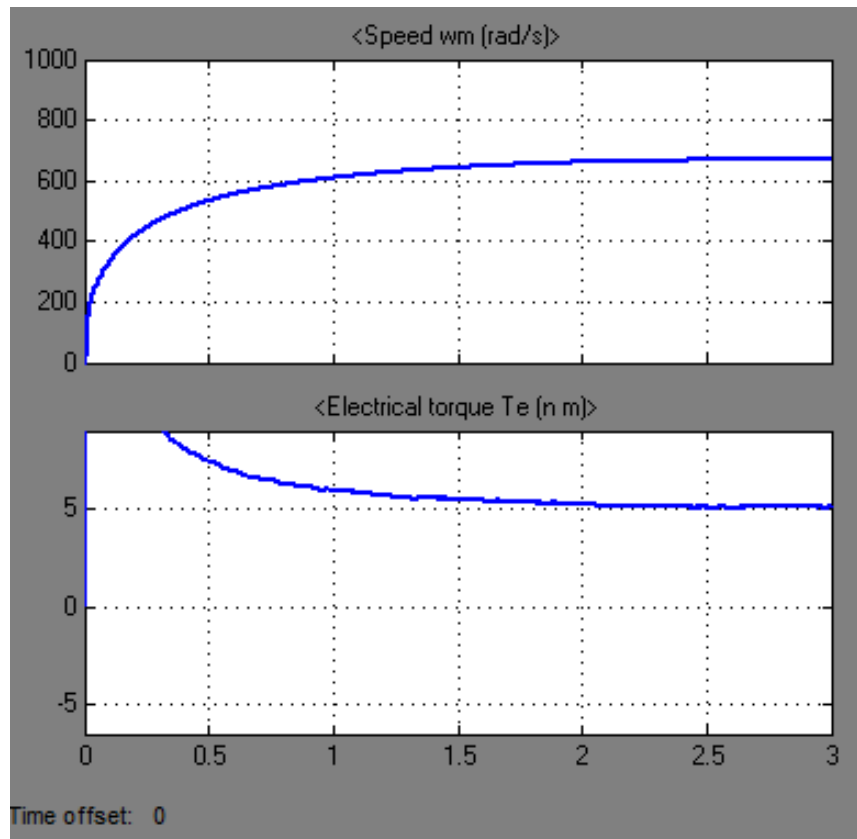


Figure 11: Characteristics of a DC motor.

Conclusion

In the above design and analysis of the ZVS interleaved booster converter is studied with complete graphs of the outputs of each source and load characteristic are explained in the paper. The output DC voltage of the ZVS converter has a gain of about 2.75 times the input voltage. Application of the EV in the smart grid with different renewable energy resources has been successfully implemented with lesser harmonics and ripples.

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